

Fig. 1

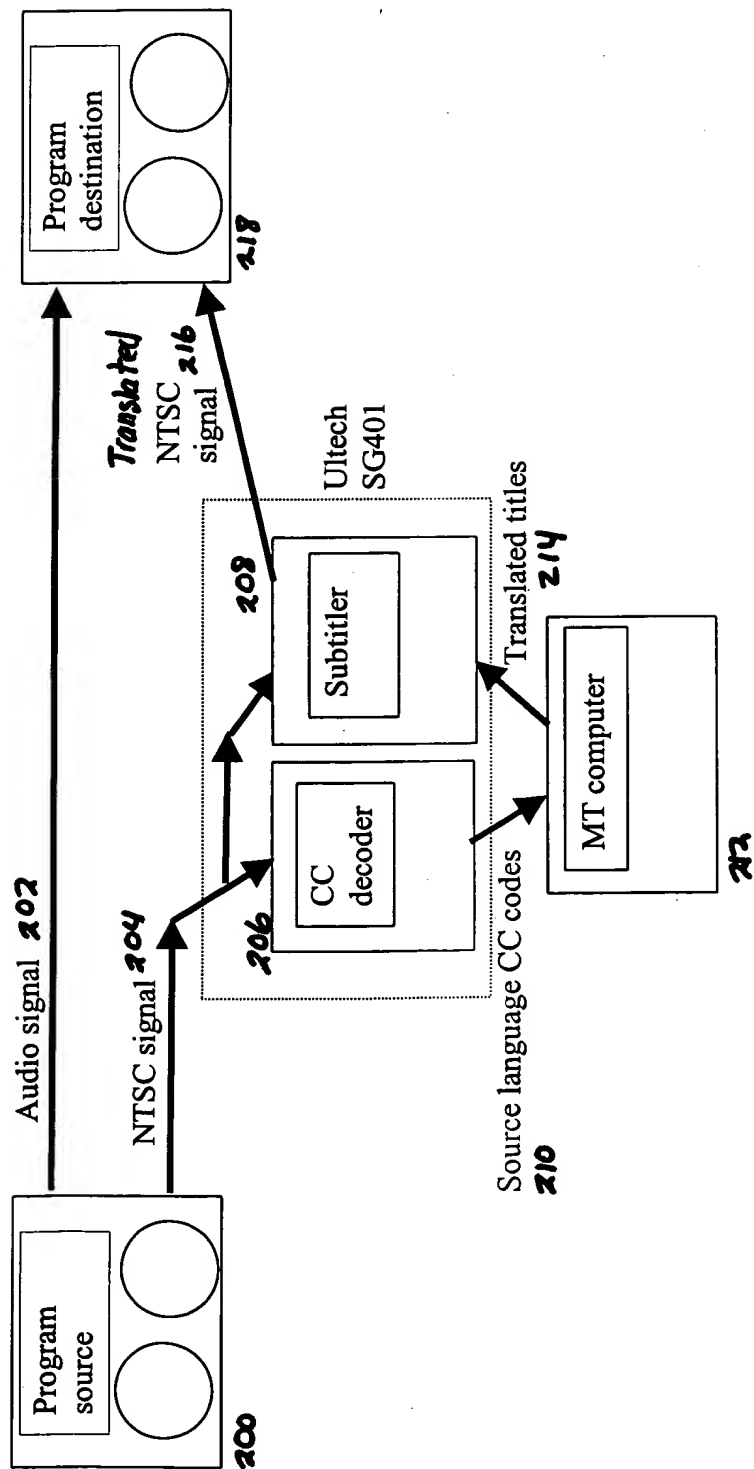


Fig. 2

FIG. 3 is a block diagram of a system for processing audio and video signals. The system includes a program source 300, a timing codes generator 310, a CC decoder 306, a CC encoder 318, an MT computer 312, and two program destinations 322 and 328. The program source 300 outputs an audio signal 302 and an NTSC signal 304. The timing codes generator 310 outputs timing codes 310 to the MT computer 312. The CC decoder 306 receives the NTSC signal 304 and outputs CC codes 308 to the MT computer 312. The MT computer 312 outputs translated CC data 314 to the CC encoder 318 and translated text 316 to the other device 324. The CC encoder 318 receives the translated CC data 314 and outputs a translated NTSC signal 320 to program destination 1 322. The other device 324 receives the translated text 316 and outputs a translated NTSC signal 326 to program destination 2 328.

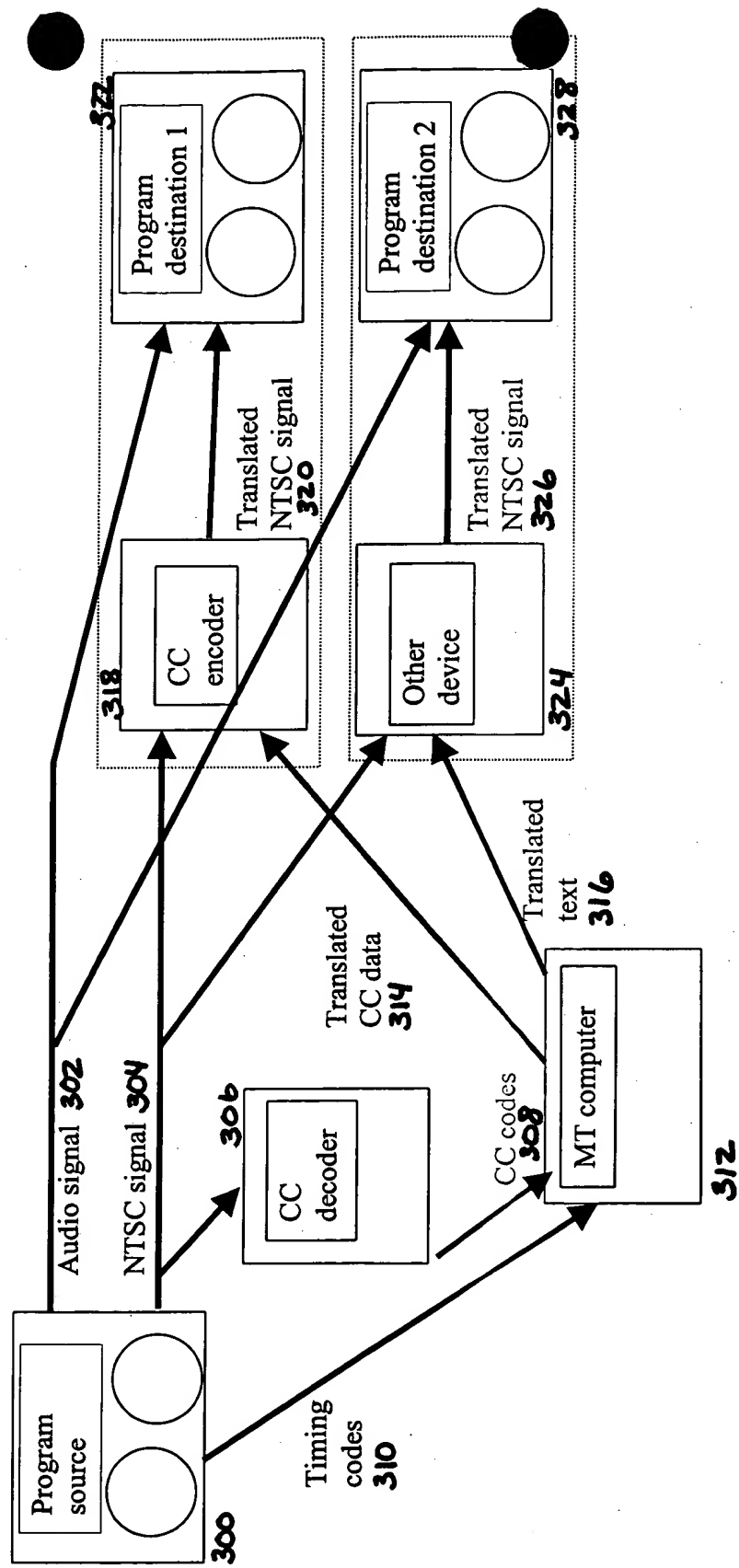


Fig. 3

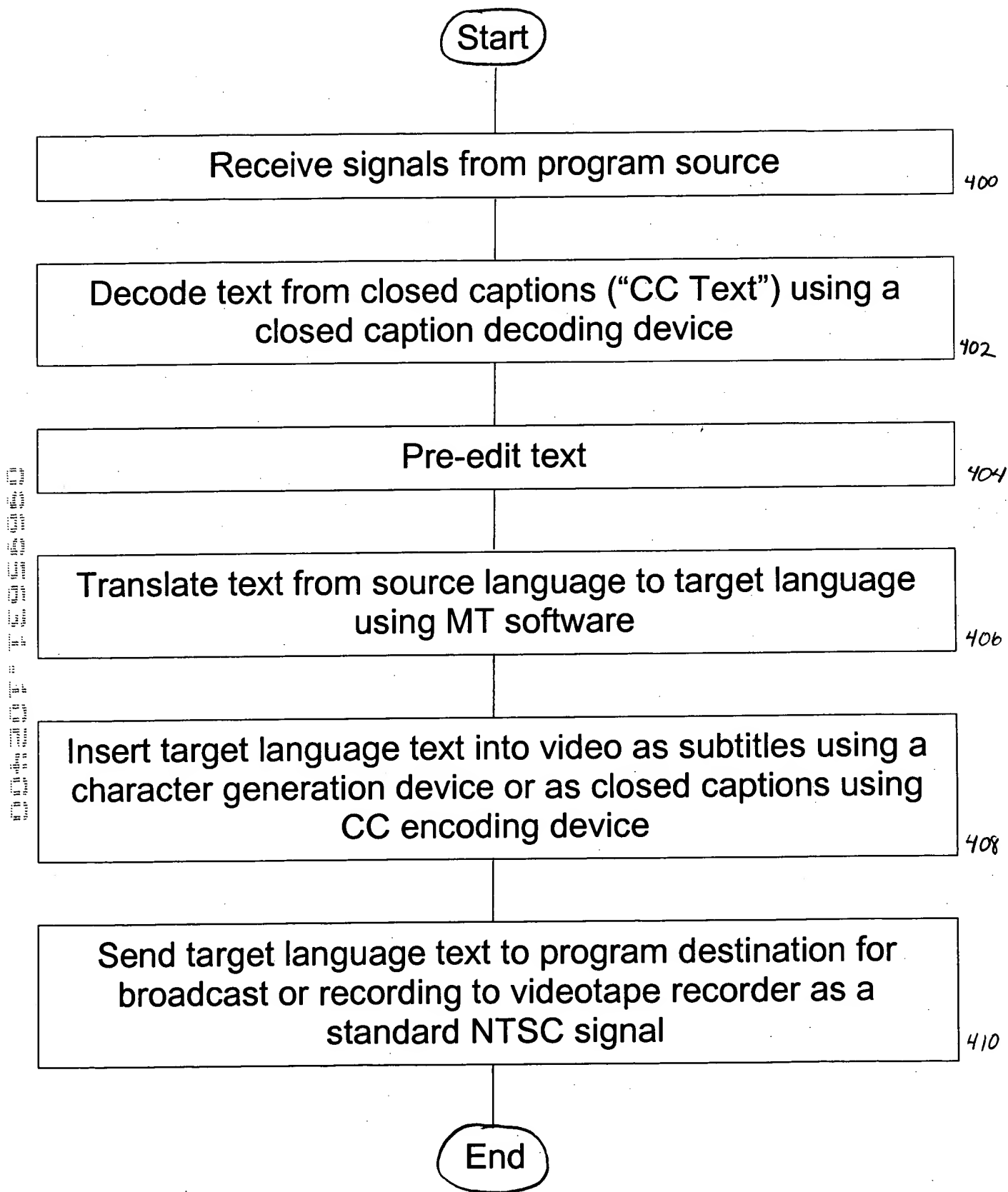


Fig. 4

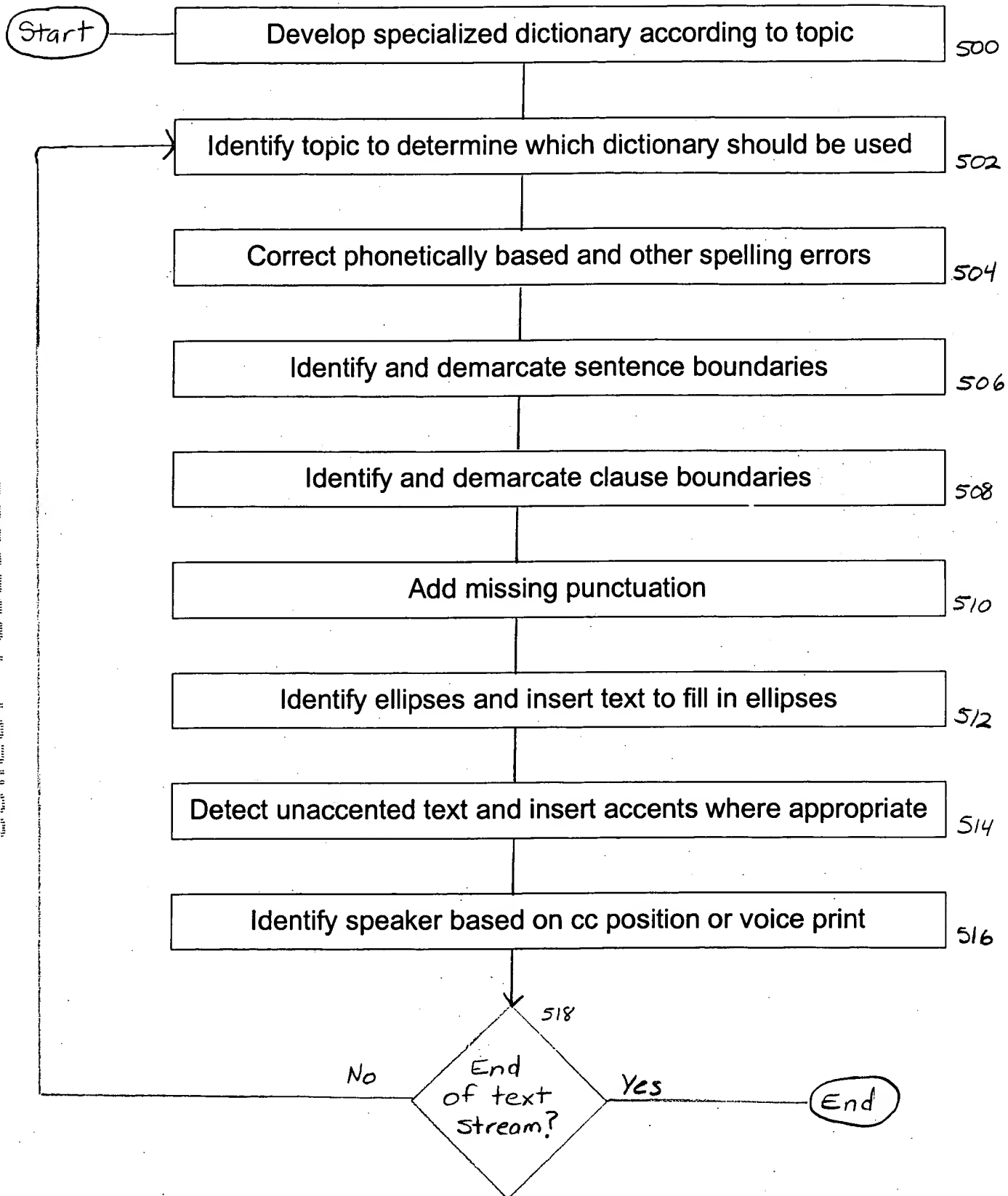


Fig. 5